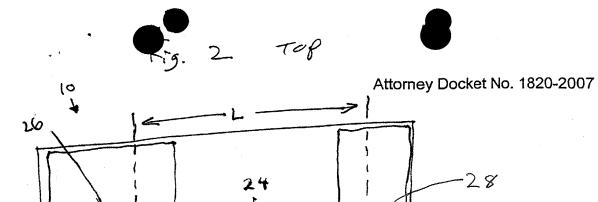
Figure 1

14 surface, substrate A12031 BeO/

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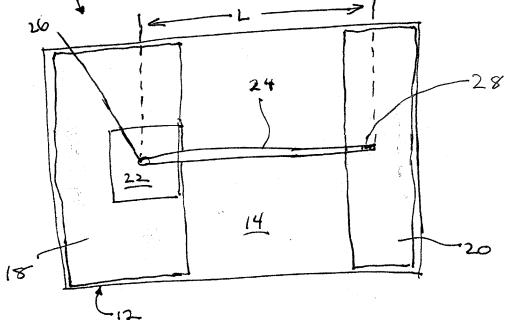


Fig. 3 bottom

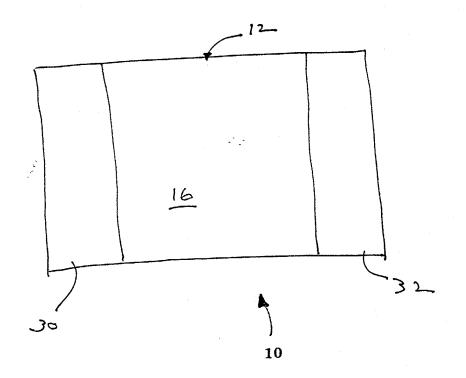
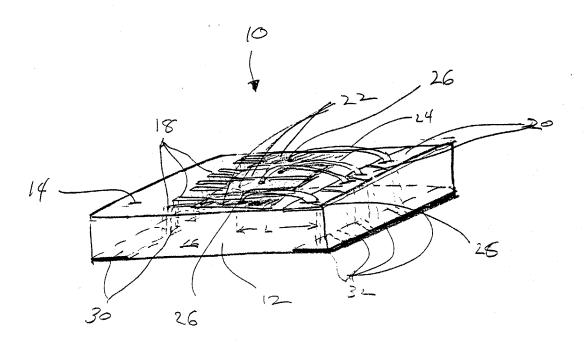


Fig. 4

top perspective view showing multiple chip (22) implementation



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## FIGURE 5

